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## EECE488: Analog CMOS Integrated Circuit Design

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CMOS Digital Integrated Circuits Analysis & Design Jan 24, 2014 by Sung-Mo (Steve) Kang Professor of Electrical and Computer Engineering and Yusuf Leblebici Professor of Electrical Engineering

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### Lecture Notes | Analysis and Design of Digital Integrated

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â€¢ DRAM Â» A capacitor to store data, and a transistor to access the capacitor

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nominal operating region is defined as the region where the gain

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### **CMOS - Wikipedia**

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